

STGW12NB60HD

N-CHANNEL 12A - 600V TO-247 PowerMESHTM IGBT

PRELIMINARY DATA

TYPE	V _{CES}	V _{CE(sat)}	Ic
STGW12NB60HD	600 V	< 2.8 V	30 A

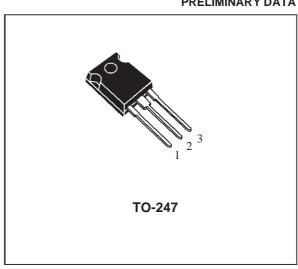
- HIGH INPUT IMPEDANCE (VOLTAGE DRIVEN)
- LOW ON-VOLTAGE DROP (VCESAT)
- LOW GATE CHARGE
- HIGH CURRENT CAPABILITY
- VERY HIGH FREQUENCY OPERATION
- OFF LOSSES INCLUDE TAIL CURRENT
- CO-PACKAGED WITH TURBOSWITCH™ ANTIPARALLEL DIODE

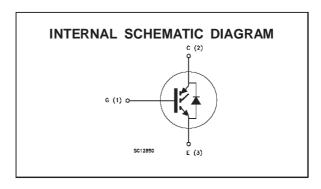
DESCRIPTION

Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESHTM IGBTs, with outstanding perfomances. The suffix "H" identifies a family optimized to achieve very low switching times for high frequency applications (<120kHz).

APPLICATIONS

- HIGH FREQUENCY MOTOR CONTROLS
- SMPS AND PFC IN BOTH HARD SWITCH AND RESONANT TOPOLOGIES
- UPS





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage (V _{GS} = 0)	600	V
V_{GE}	Gate-Emitter Voltage	± 20	V
Ic	Collector Current (continuous) at T _c = 25 °C	24	А
Ic	Collector Current (continuous) at T _c = 100 °C	12	Α
I _{CM} (•)	Collector Current (pulsed)	96	Α
P _{tot}	Total Dissipation at T _c = 25 °C	120	W
	Derating Factor	0.96	W/°C
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

June 1999 1/8

THERMAL DATA

R _{thj-case} R _{thj-amb}		Junction-case Junction-ambient	Max Max	1.04 30	°C/W oC/W
R _{thc-h}		Case-heatsink	Тур	0.1	°C/W

ELECTRICAL CHARACTERISTICS ($T_j = 25$ °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{BR(CES)}	Collector-Emitter Breakdown Voltage	$I_C = 250 \ \mu A$ $V_{GE} = 0$	600			V
I _{CES}	Collector cut-off (V _{GE} = 0)	$V_{CE} = Max Rating$ $T_j = 25 ^{\circ}C$ $V_{CE} = Max Rating$ $T_j = 125 ^{\circ}C$			250 2000	μA μA
I _{GES}	Gate-Emitter Leakage Current (V _{CE} = 0)	$V_{GE} = \pm 20 \text{ V}$ $V_{CE} = 0$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{GE(th)}	Gate Threshold Voltage	$V_{CE} = V_{GE}$ $I_C = 250 \mu A$	3		5	V
V _{CE(SAT)}	Collector-Emitter Saturation Voltage	V _{GE} = 15 V I _C = 12 A V _{GE} = 15 V I _C = 12 A T _j = 125 °C		2 1.7	2.8	V

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
	Forward Transconductance	V _{CE} =25 V I _C = 12 A		9.5		S
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{CE} = 25 V f = 1 MHz V _{GE} = 0		920 120 27		pF pF pF
Q _G Q _{GE} Q _{GC}	Total Gate Charge Gate-Emitter Charge Gate-Collector Charge	V _{CE} = 480 V I _C = 12 A V _{GE} = 15 V		68 10 30		nC nC nC
I _{CL}	Latching Current	$V_{clamp} = 480 \text{ V } R_G = 10\Omega T_j = 150 ^{o}\text{C}$	48			А

SWITCHING ON

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
t _{d(on)}	Delay Time Rise Time	V _{CC} = 480 V V _{GE} = 15 V	$I_C = 12 A$ $R_G = 10\Omega$		5 46		ns ns
(di/dt) _{on}	Turn-on Current Slope	$V_{CC} = 480 \text{ V}$ $R_G = 10 \Omega$	$I_{C} = 12 A$ $V_{GE} = 15 V$		800		A/μs
E _{on} (o)	Turn-on Switching Losses	T _j = 125 °C			290		μJ

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ELECTRICAL CHARACTERISTICS (continued)

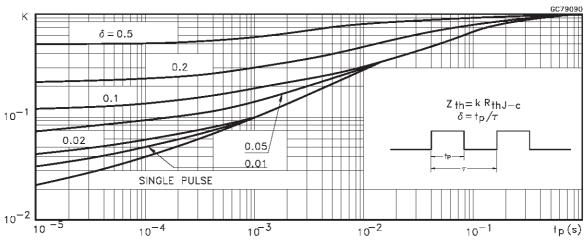
SWITCHING OFF

Symbol	Parameter	Test Co	nditions	Min.	Тур.	Max.	Unit
$\begin{array}{c} t_{c} \\ t_{r}(v_{off}) \\ t_{d}(_{off}) \\ t_{f} \end{array}$	Cross-Over Time Off Voltage Rise Time Delay Time Fall Time	$V_{CC} = 480 \text{ V}$ $R_{GE} = 10 \Omega$	$I_C = 12 A$ $V_{GE} = 15 V$		150 27 76 92		ns ns ns ns
E _{off} (**) E _{ts} (0)	Turn-off Switching Loss Total Switching Loss				0.21 0.49		mJ mJ
$\begin{array}{c} t_{c} \\ t_{r}(v_{off}) \\ t_{d}(off) \\ t_{f} \\ E_{off}(^{**}) \\ E_{ts}(o) \end{array}$	Cross-Over Time Off Voltage Rise Time Delay Time Fall Time Turn-off Switching Loss Total Switching Loss	$V_{CC} = 480 \text{ V}$ $R_{GE} = 10 \Omega$ $T_{j} = 125 \text{ °C}$	I _C = 12 A V _{GE} = 15 V		229 76 95 200 0.45 0.74		ns ns ns ns mJ mJ

COLLECTOR-EMITTER DIODE

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit	
I _f	Forward Current Forward Current pulsed					12 96	A A
V _f	Forward On-Voltage	I _f = 12 A I _f = 12 A	T _j = 125 °C		1.55 1.3	2.0	V
t _{rr} Q _{rr} I _{rrm}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$\begin{array}{l} I_f = 12 \text{ A} & V_c \\ di/dt = 100 \text{ A}/\mu\text{S} \end{array}$	$T_{j} = 125 {}^{\circ}\text{C}$		100 330 6.3		nS nC A

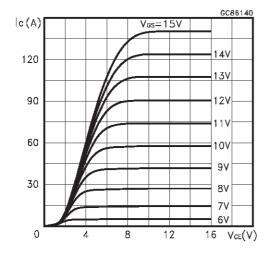
Thermal Impedance



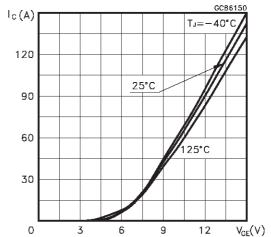
^(•) Pulse width limited by max. junction temperature
(b) Include recovery losses on the STTA1206 freewheeling diode

^(*) Pulsed: Pulse duration = 300 µs, duty cycle 1.5 % (**)Losses Include Also The Tail (Jedec Standardization)

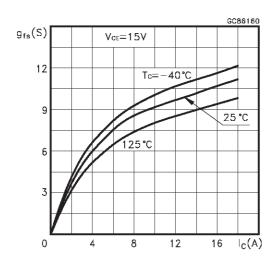
Output Characteristics



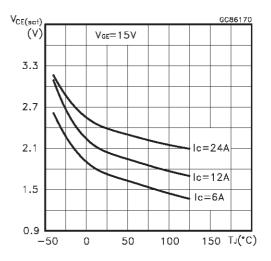
Transfer Characteristics



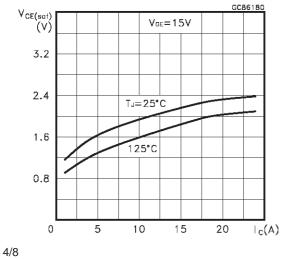
Transconductance



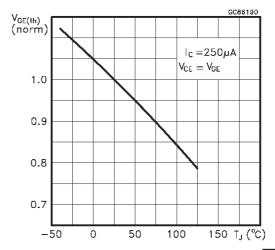
Collector-Emitter On Voltage vs Temperature



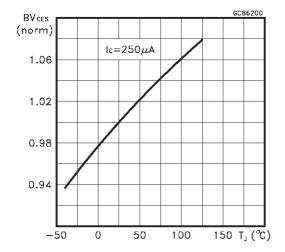
Collector-Emitter On Voltage vs Collector Current



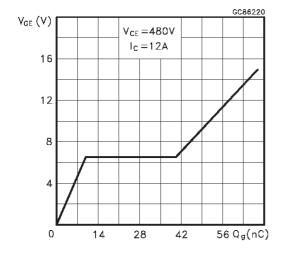
Gate Threshold vs Temperature



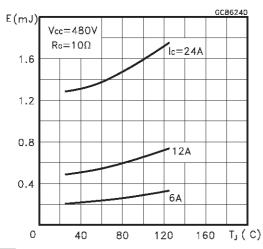
Normalized Breakdown Voltage vs Temperature



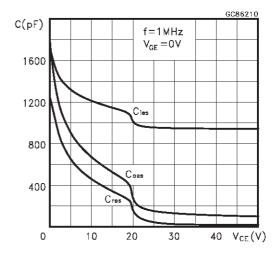
Gate Charge vs Gate-Emitter Voltage



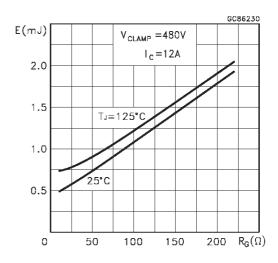
Total Switching Losses vs Temperature



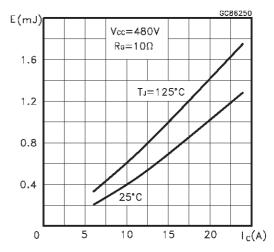
Capacitance Variations



Total Switching Losses vs Gate Resistance



Total Switching Losses vs Collector Current



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Switching Off Safe Operating Area

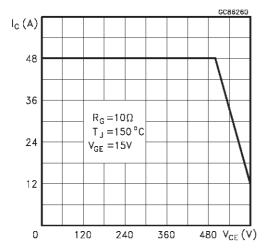


Fig. 1: Gate Charge test Circuit

Diode Forward Voltage

►A OA

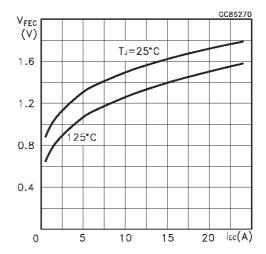
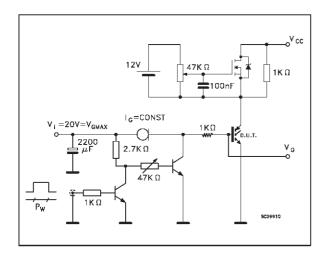


Fig. 2: Test Circuit For Inductive Load Switching



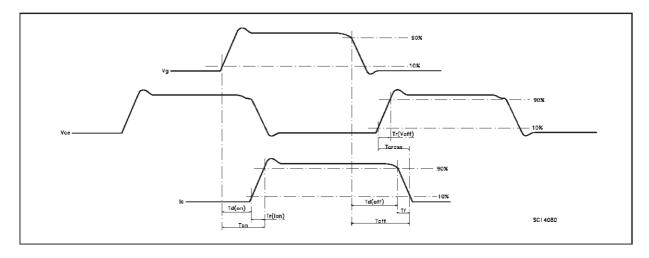
FAST DIODE L=100μH

3.3 1000 μF

V_{CC}

SC09920

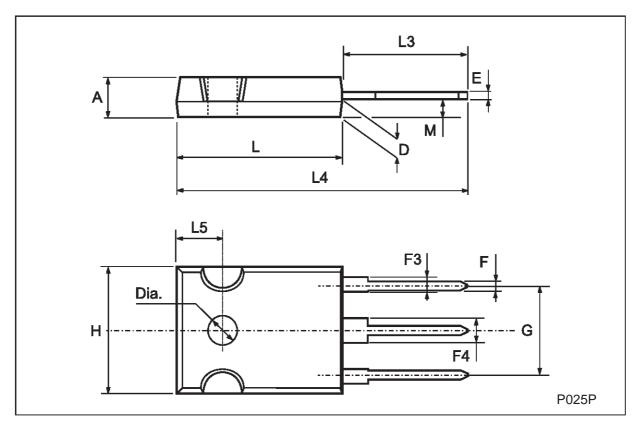
Fig. 3: Switching Waveforms



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TO-247 MECHANICAL DATA

DIM.		mm			inch	
DIWI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.7		5.3	0.185		0.209
D	2.2		2.6	0.087		0.102
E	0.4		0.8	0.016		0.031
F	1		1.4	0.039		0.055
F3	2		2.4	0.079		0.094
F4	3		3.4	0.118		0.134
G		10.9			0.429	
Н	15.3		15.9	0.602		0.626
L	19.7		20.3	0.776		0.779
L3	14.2		14.8	0.559		0.582
L4		34.6			1.362	
L5		5.5			0.217	
М	2		3	0.079		0.118



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